TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74AC151P,TC74AC151F,TC74AC151FN

8-Channel Multiplexer

The TC74AC151 is an advanced high speed CMOS 8-CHANNEL MULTIPLEXER fabricated with silicon gate and double-layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

One of eight date input signals (D0-D7) is selected by decoding of the three-bit address input (A, B, C). The selected data appears on two outputs: non-inverting (Y) and inverting (W).

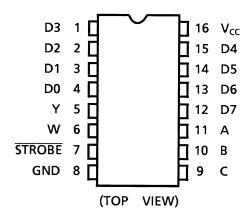
The \overline{STROBE} input provides two output conditions; a low level on the \overline{STROBE} input transferrs the selected data to the outputs. A high level on the \overline{STROBE} input sets the Y output low and the W output high without regard to the data or select input conditions.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

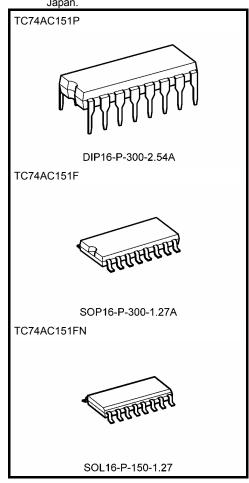
Features

- High speed: $t_{pd} = 5.3 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 8 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24$ mA (min) Capability of driving 50 Ω transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74F151

Pin Assignment



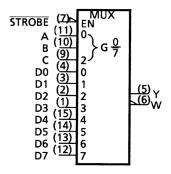
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

IEC Logic Symbol

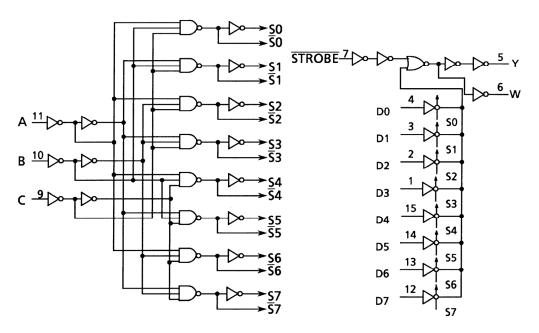


Truth Table

	I	Outputs			
Select			STROBE	Y	W
С	В	Α	SIROBE	ī	VV
Х	Х	Х	Н	L	Н
L	L	L	L	D0	D0
L	L	Н	L	D1	D1
L	Н	L	L	D2	D2
L	Н	Н	L	D3	D3
Н	L	L	L	D4	Ū4
Н	L	Н	L	D5	D ₅
Н	Н	L	L	D6	D̄6
Н	Н	Н	L	D7	D̄7

X: Don't care

System Diagram



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Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±100	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to $65^{\circ}C$. From Ta = 65 to $85^{\circ}C$ a derating factor of -10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0 to 5.5	V	
Input voltage	V _{IN}	0 to V _{CC}	V	
Output voltage	V _{OUT}	0 to V _{CC}	V	
Operating temperature	T _{opr}	−40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
input rise and fail tille	ui/uv	0 to 20 (V _{CC} = 5 ± 0.5 V)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

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Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Cymbol				V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
	V _{IH}	_			2.0	1.50	_	_	1.50	_	
High-level input voltage				3.0	2.10	_	_	2.10	_	V	
				5.5	3.85	_	_	3.85	_		
		_		2.0	_	_	0.50	_	0.50		
Low-level input voltage	V_{IL}			3.0	_	_	0.90	_	0.90	V	
				5.5	_	_	1.65	_	1.65		
	V _{ОН}	V _{IN} = V _{IH} or V _{IL}			2.0	1.9	2.0	_	1.9	_	
			$I_{OH} = -50 \mu A$		3.0	2.9	3.0	_	2.9	_	
High-level output				4.5	4.4	4.5	_	4.4	_	V	
voltage			$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	V
			$I_{OH} = -24 \text{ mA}$		4.5	3.94	_	_	3.80	_	
			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	_	—	_	3.85	_	
	V _{OL}	VIN = VIH or VIL			2.0	_	0.0	0.1	_	0.1	
			$I_{OL} = 50 \mu A$		3.0	_	0.0	0.1	_	0.1	
Low-level output voltage					4.5	_	0.0	0.1	_	0.1	V
			I _{OL} = 12 mA		3.0	_	_	0.36		0.44	· I
			I _{OL} = 24 mA		4.5	_	_	0.36		0.44	
			$I_{OL} = 75 \text{ mA}$	(Note)	5.5	_	_	_		1.65	
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		5.5			±0.1		±1.0	μА	
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	8.0	_	80.0	μА	

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.



AC Characteristics (CL = 50 pF, RL = 500 Ω , input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	- ,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3	_	10.7	19.3	1.0	22.0	ns
(D-Y, W)	t _{pHL}		5.0 ± 0.5	_	6.6	10.5	1.0	12.0	
Propagation delay time	t _{pLH}	_	3.3 ± 0.3 5.0 ± 0.5	_	13.3 8.2	23.7	1.0 1.0	27.0 14.8	ns
(A, B, C-Y, W)	γпс		0.0 = 0.0		0.2	10.0	1.0	11.0	
Propagation delay time (ST -Y, W)	t _{pLH} t _{pHL}	_	3.3 ± 0.3 5.0 ± 0.5	_ _	8.6 5.6	15.3 9.6	1.0 1.0	18.0 11.0	ns
Input capacitance	C _{IN}	_	•		5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)	_			68	_	_	_	pF

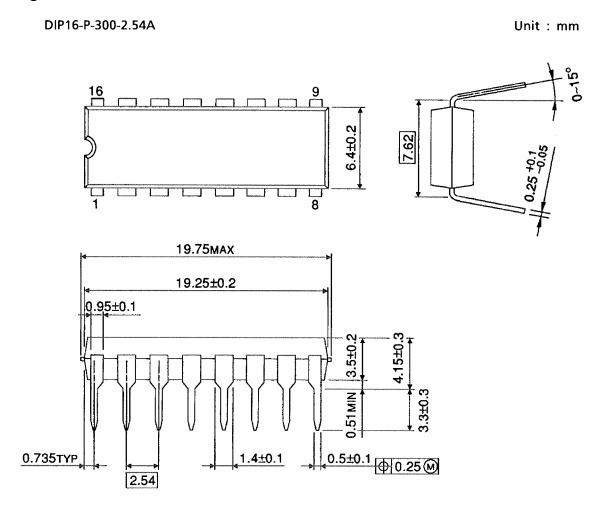
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

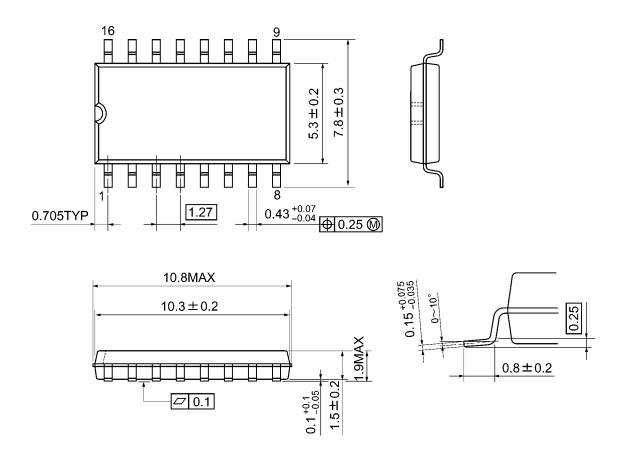
Package Dimensions



Weight: 1.00 g (typ.)

Package Dimensions

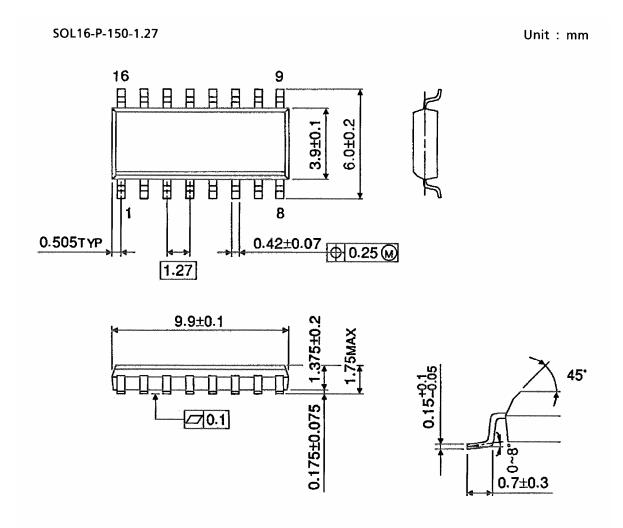
SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



Package Dimensions (Note)



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Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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